

**What is claimed is:**

1. A liquid crystal display device comprising:
  - upper and lower substrates with a liquid crystal layer interposed therebetween;
  - a sealant between the upper and lower substrates;
  - a plurality of source and gate pads on the lower substrate;
  - a plurality of gate and data lines on the lower substrate, each gate line being electrically connected with the corresponding gate pad, each data line being electrically connected with the corresponding source pad;
  - a gate insulating layer between the gate lines and the data lines;
  - a source PCB electrically connected with the plurality of source pads;
  - a gate PCB electrically connected with the plurality of gate pads; and
  - a plurality of transmitting wires on the lower substrate, the transmitting wires being electrically connected with the gate and source pads across the sealant such that the source PCB is electrically connected with the gate PCB.
2. The device of claim 1, further comprising a plurality of switching devices.
3. The device of claim 1, wherein the gate transmitting wires include at least eight electrical wires.
4. The device of claim 1, further comprising a plurality of dummy pads between the adjacent gate pads and between the adjacent source pads.
5. The device of claim 1, further comprising a repair wire crossing with each

gate transmitting wire with the gate insulating layer interposed between the repair wire and the gate transmitting wire.

6. The device of claim 5, wherein a specific resistance of the repair wire is below  $10\mu\Omega/\text{cm}$  inclusive.
7. The device of claim 5, wherein the repair wire is positioned in a region defined by the sealant.
8. The device of claim 5, wherein the repair wire is positioned across the sealant.
9. The device of claim 5, wherein the repair wire includes first and second closed roofs, the first closed roof being formed along first edge of the upper substrate, the second closed roof being formed along second edge of the upper substrate.
10. The device of claim 1, further comprising first and second repair wire, the first repair wire crossing with each of the source pads with the gate insulating layer interposed therebetween, the second repair wire crossing with each of the gate pads with the gate insulating layer interposed therebetween.
11. The device of claim 10, wherein a specific resistance of the first and second repair wires is below  $10\mu\Omega/\text{cm}$  inclusive.

12. The device of claim 1, further comprising first and second dummy patterns on the lower substrate, the first dummy pattern being positioned along a first edge of the upper substrate, the second dummy pattern being positioned along a second edge of the upper substrate, the each dummy pattern having at least the same height as the gate transmitting wire.

13. The device of claim 12, further comprising auxiliary dummy patterns over the first and second dummy patterns.

14. A method of fabricating a liquid crystal display device, the method comprising:

preparing first and second substrates;

forming a plurality of gate lines, gate pads, and dummy patterns on the first substrate;

forming a gate insulating layer on the gate lines, gate pads, and dummy patterns;

forming a plurality of data lines, data pads, and gate transmitting wires on the gate insulating layer;

forming a passivation layer on the data lines, the data pads, and the gate transmitting wires;

forming a sealant on the first substrate;

attaching the first and second substrates;

scribing and breaking the second substrate; and